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1 Performance-Area Trade-Off of Address Generators for Address Decoder-Decoupled Memory
S. Hettiaratchi, P. Cheung, T. Clarke
March 2002 **Proceedings of the conference on Design, automation and test in Europe**
Publisher: IEEE Computer Society
Full text available: pdf(128.35 KB)
Publisher Site
Additional Information: full citation, abstract, citings

Multimedia applications are characterized by a largenumber of data accesses and complex array index manipulations.The built-in address decoder in the RAM memorymodel commonly used by most memory synthesis tools, unnecessarilyrestricts the freedom of address generator synthesis.Therefore a memory model in which the address decoderis decoupled from the memory cell array is proposed.In order to demonstrate the benefits and limitations of thisalternative memory model, synthesis results for a Shift R ...

2 Design of Nonlinear CA Based TPG Without Prohibited Pattern Set In Linear Time
Sukanta Das, Anirban Kundu, Biplab K. Sikdar, P. Pal Chaudhuri
January 2005 **Journal of Electronic Testing: Theory and Applications**, Volume 21 Issue 1
Publisher: Kluwer Academic Publishers
Additional Information: full citation, abstract, index terms

This paper reports an efficient BIST solution for VLSI circuits. The solution is based on an on-chip Pseudo-Random Pattern Generator (PRPG) for the CUTs (Circuit Under Test) of a VLSI chip that may be accessed through a full or partial scan path. The test solution guarantees non-issuance of the test patterns declared pro ...

Keywords: TPG, nonlinear CA, prohibited pattern set

3 Algorithm 744: a stochastic algorithm for global optimization with constraints
F. Michael Rabinowitz
June 1995 **ACM Transactions on Mathematical Software (TOMS)**, Volume 21 Issue 2
Publisher: ACM Press
Full text available: pdf(1.30 MB)
Additional Information: full citation, abstract, references, index terms, review

A stochastic algorithm is presented for finding the global optimum of a function of n variables subject to general constraints. The algorithm is intended for moderate values of n, but it can accommodate objective and constraint functions that are discontinuous and

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1   Joint session: Coloring unstructured radio networks

Thomas Moscibroda, Roger Wattenhofer

July 2005   **Proceedings of the 17th annual ACM symposium on Parallelism in algorithms and architectures SPAA'05**

Publisher: ACM Press

Full text available: 📄 pdf(232.35 KB)   Additional Information: full citation, abstract, references, index terms

During and immediately after their deployment, ad hoc and sensor networks lack an efficient communication scheme rendering even the most basic network coordination problems difficult. Before any reasonable communication can take place, nodes must come up with an initial structure that can serve as a foundation for more sophisticated algorithms. In this paper, we consider the problem of obtaining a vertex coloring as such an initial structure. We propose an algorithm that works under the unstruct ...

Keywords: ad hoc networks, asynchronous wake-up, coloring, initialization, radio network, sensor networks

2   Stimulus-response machines: a new visual formalism for describing classes and objects

George W. Cherry

April 1993   **ACM SIGSOFT Software Engineering Notes**, Volume 18 Issue 2

Publisher: ACM Press

Full text available: 📄 pdf(828.98 KB)   Additional Information: full citation, index terms

3   Self-stabilization by counter flushing

George Varghese

August 1994   **Proceedings of the thirteenth annual ACM symposium on Principles of distributed computing**

Publisher: ACM Press

Full text available: 📄 pdf(1.02 MB)   Additional Information: full citation, references, citings, index terms

4   Stimulating cooperation in self-organizing mobile ad hoc networks

Levente Buttyán, Jean-Pierre Hubaux

October 2003   **Mobile Networks and Applications**, Volume 8 Issue 5

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☐   **1. Superconducting instrument systems**
Goodman, W.L.; Hesterman, V.W.; Rorden, L.H.; Goree, W.S.;
Proceedings of the IEEE
Volume 61, Issue 1, Jan. 1973 Page(s):20 - 27
AbstractPlus | Full Text: PDF(1473 KB)   IEEE JNL

☐   **2. CODEST: A New Pulse-Code Modulation System for Telegraph and Data Tran:**
Oswald, J.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 18, Issue 3, Jun 1970 Page(s):223 - 233
AbstractPlus | Full Text: PDF(1128 KB)   IEEE JNL

☐   **3. A moving-window detector for binary integration**
Dillard, G.;
Information Theory, IEEE Transactions on
Volume 13, Issue 1, Jan 1967 Page(s):2 - 6
AbstractPlus | Full Text: PDF(776 KB)   IEEE JNL

☐   **4. An analysis of signal detection and location by digital methods**
Dinneen, G.; Reed, I.;
Information Theory, IEEE Transactions on
Volume 2, Issue 1, Mar 1956 Page(s):29 - 38
AbstractPlus | Full Text: PDF(1424 KB)   IEEE JNL

☐   **5. A 45-Mbit/s CMOS VLSI digital phase aligner**
Cordell, R.R.;
Solid-State Circuits, IEEE Journal of
Volume 23, Issue 2, April 1988 Page(s):323 - 328
Digital Object Identifier 10.1109/4.991
AbstractPlus | Full Text: PDF(556 KB)   IEEE JNL

☐   **6. Josephson counting analog-to-digital converter**
Miller, D.L.; Przybysz, J.X.; Kang, J.; Hamilton, C.A.; Burnell, D.M.;
Magnetics, IEEE Transactions on
Volume 27, Issue 2, Part 4, Mar 1991 Page(s):2761 - 2764
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AbstractPlus | Full Text: PDF(304 KB)   IEEE JNL

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1   Efficient algorithms for bidirectional debugging

Bob Boothe

May 2000   **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2000 conference on Programming language design and implementation PLDI '00**, Volume 35 Issue 5

Publisher: ACM Press

Full text available: 📄 pdf(473.76 KB)   Additional Information: full citation, abstract, references, citings, index terms

> This paper discusses our research into algorithms for creating an efficient bidirectional debugger in which all traditional forward movement commands can be performed with equal ease in the reverse direction. We expect that adding these backwards movement capabilities to a debugger will greatly increase its efficacy as a programming tool. The efficiency of our methods arises from our use of event counters that are embedded into the program being debugged. These counters are used ...

2   High-Level Language Implications of the Proposed IEEE Floating-Point Standard

Richard J. Fateman

April 1982   **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 4 Issue 2

Publisher: ACM Press

Full text available: 📄 pdf(1.26 MB)   Additional Information: full citation, references, index terms

3   An extensible probe architecture for network protocol performance measurement

G. Robert Malan, Farnam Jahanian

October 1998 **ACM SIGCOMM Computer Communication Review , Proceedings of the ACM SIGCOMM '98 conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM '98**, Volume 28 Issue 4

Publisher: ACM Press

Full text available: 📄 pdf(1.83 MB)   Additional Information: full citation, abstract, references, citings, index terms

> This paper describes the architecture and implementation of Windmill, a passive network protocol performance measurement tool. Windmill enables experimenters to measure a broad range of protocol performance metrics by both reconstructing application-level network protocols and exposing the underlying protocol layers' events. Windmill is split into three functional components: a dynamically compiled Windmill Protocol Filter (WPF), a set of abstract protocol modules, and an extensible experiment e ...